

## Maxon Smp 4004c Programming Software

to Zero - Develop a Weighing System in C#, Javascript, PHP, and VB.Net4min read program reads a series of different weights from a keyboard and converts them into a single overall weight, saves this data in the format that the user chooses and then calculates the proper postage. The total amount of postage is calculated and put into a text file as well as being displayed to the user.The fullscreen version works with the same exact functionality as the normal download.The invention relates to the field of microelectronic integrated circuits. The invention relates more particularly to a method for fabricating a microelectronic integrated circuit including a multi-gate finFET. An example of a finFET (hereinafter finFET) is illustrated in FIG. 1. In this figure, reference numeral 10 denotes a substrate; 11 denotes a fin that extends from the substrate 10; 12 denotes a gate dielectric layer formed on a top surface of the fin 11; 13 denotes a gate electrode material; 14 denotes a cap layer that is formed on the gate electrode material 13; 15 denotes a source region; 16 denotes a drain region; and 17 denotes a channel region, the fin 11 being located between the source region 15 and the drain region 16. FinFETs are becoming of interest for their improved performance, in particular for low leakage currents. In finFETs, the gate electrode material is applied over the fin 11 and extends down into the channel region 17 of the fin 11. As a result of this, the channel region 17 of the fin 11 has a higher current density than in conventional planar MOS transistors. Moreover, the drain and source regions 16, 15 are at the ends of the fin 11 and therefore exhibit a more favorable resistance against high electric fields, which is desirable in order to limit the leakage currents. In finFETs, the gate dielectric layer is formed in a trench 18 that is generally formed in the fin 11 in order to make contact with the gate electrode material 13. FinFETs are fabricated in CMOS technology. In CMOS technology, for a given gate length, it is desirable to improve the leakage currents by modifying the fin structures. FIG. 2 illustrates an example of a microelectronic integrated circuit 2 that includes a conventional finFET. The integrated circuit 2 is formed on a substrate 10. The integrated circuit 2 includes an array of finFETs 10a, 10b.... Each finFET 10a, 10b... is formed over a buried oxide layer 20. A polysilicon gate electrode material 13 is formed over the finFET 10a, 10b... that each include a fin 11 and a trench 18 in which the gate dielectric layer

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